



Attorney Docket No.: BHT/3212-38

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : YANG
Application No. : 10/648,195
Filed : August 27, 2003
Title : INTERRUPT-PROCESSING SYSTEM FOR SHORTENING
INTERRUPT LATENCY IN MICROPROCESSOR
Group Art Unit : 2111
Examiner : C. Daley
Docket No. : 3212-38

MAIL STOP - ISSUE FEE

Honorable Commissioner for Patents
Washington, D.C. 20231

RESPONSE UNDER 37 C.F.R. §1.312

Sir:


In reviewing the documents for this application, it was noted that Applicant's claim to priority was not properly recorded on the Official Filing Receipt. Attached hereto is a copy of the receipt card from the initial filing of this application bearing the U.S. Patent and Trademark Office date-stamp of August 27, 2003 indicating that a claim for priority and a priority document were filed on that date. Also enclosed is a copy of the Declaration submitted with the original application papers claiming priority from Taiwan Patent Application No. 092105040.

It is requested that the U.S. Patent and Trademark Office records be corrected to show that Applicant has properly claimed priority in this application and that the priority information is properly listed on the printed Letters Patent.

Respectfully submitted,

Date: March 15, 2006

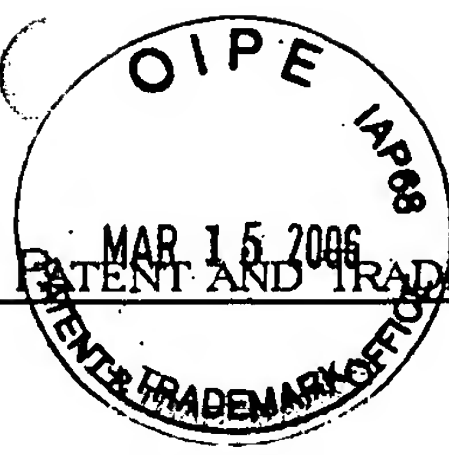
By:


Bruce H. Troxell
Reg. No. 26,592

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UNITED STATES PATENT AND TRADEMARK OFFICE



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APPL NO.	FILING OR 371 (c) DATE	ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO	DRAWINGS	TOT CLMS	IND CLMS
10/648,195	08/27/2003	2189	375	BHT-3212-38	5	14	1

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FALLS CHURCH, VA 22041

Your Ref.
SNVT200204US

CONFIRMATION NO. 3475

FILING RECEIPT



OC000000011305001

Date Mailed: 11/19/2003

5/C 8-27-04

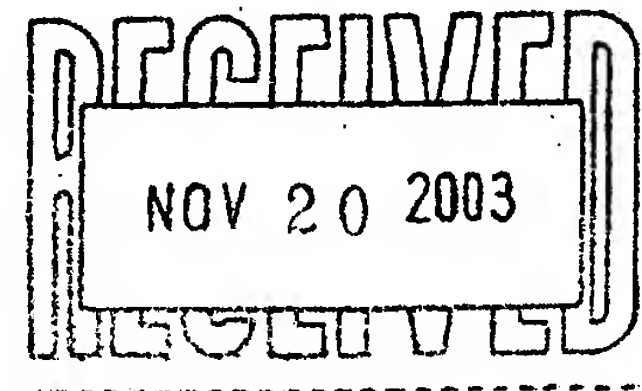
Receipt is acknowledged of this regular Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Filing Receipt Corrections, facsimile number 703-746-9195. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

Applicant(s)

Pachinco Yang, HsinChu, TAIWAN;

Assignment For Published Patent Application

Novatek Microelectronic Co., Corp.

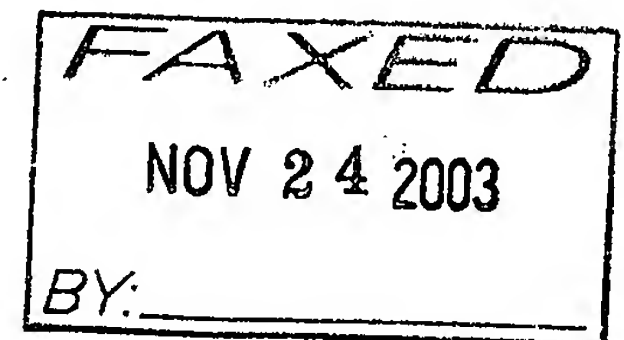


Domestic Priority data as claimed by applicant

Foreign Applications

TAIWAN 092105040. filed 7-March-2003

If Required, Foreign Filing License Granted: 11/18/2003



Projected Publication Date: 03/03/2005

Non-Publication Request: No

Early Publication Request: No

LARGE

** SMALL ENTITY **

Title

Interrupt-processing system for shortening interrupt latency in microprocessor



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S/C 8-27-04

CLIENT: IPTEC Inc.
Inventor: Pachinco YANG

Client Ref: 5NVT200204US
Docket No: BHT-3212-38

Title: INTERRUPT-PROCESSING SYSTEM FOR SHORTENING INTERRUPT LATENCY IN MICROPROCESSOR

Date filed: August 27, 2003

Initials: BHT/mbh

Transmitted herewith for filing are the following:

1. Transmittal cover sheet for New U.S. Patent application (37 CFR 1.53b)
2. 14 pages of Specification; 14 Claims
3. 5 sheets of Drawings; Figs 1-5
4. Combined Declaration & Appt. of Atty.
5. Assignment w/PTO-1595
6. Claim for Priority and Priority Document
7. Check No. 8204 For \$415.00.
8. Early Notification Serial No. Postcard.



Receipt is hereby acknowledged by the U.S. Patent & Trademark Office of the above-identified papers.

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE



In re Patent Application of

Pachinco YANG

Application No.: Not Yet Assigned

Filed: August 27, 2003

Group Art Unit: Not Yet Assigned

Examiner: Not Yet Assigned

For: **INTERRUPT-PROCESSING SYSTEM FOR SHORTENING INTERRUPT
LATENCY IN MICROPROCESSOR**

CLAIM TO PRIORITY UNDER 35 U.S.C. § 119

Assistant Commissioner of Patents
Washington, D.C. 20231

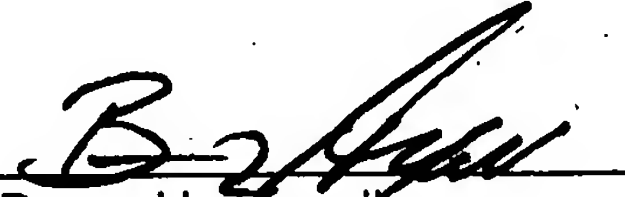
Sir:

Pursuant to the provisions of 35 U.S.C. § 119 and 37 C.F.R. § 1.55, Applicant
claims the right of priority based upon **Chinese Application No. 092105040** filed
March 7, 2003.

A certified copy of Applicant's priority document is submitted herewith.

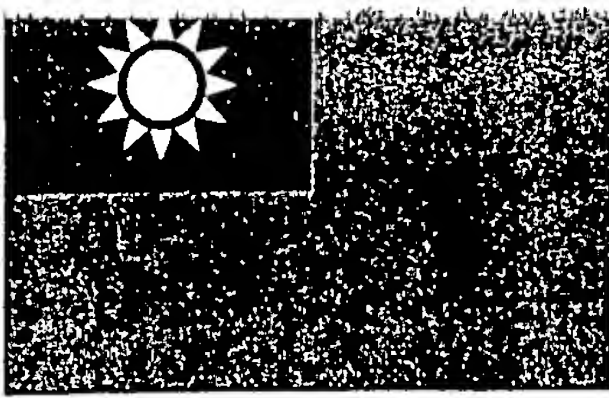
Respectfully submitted,

By:


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Date: August 27, 2003



中華民國經濟部智慧財產局

INTELLECTUAL PROPERTY OFFICE
MINISTRY OF ECONOMIC AFFAIRS
REPUBLIC OF CHINA

茲證明所附文件，係本局存檔中原申請案的副本，正確無訛，
其申請資料如下：

This is to certify that annexed is a true copy from the records of this
office of the application as originally filed which is identified hereunder

申請日：西元 2003 年 03 月 07 日
Application Date

申請案號：092105040
Application No.

申請人：聯詠科技股份有限公司
Applicant(s)

局 長
Director General

蔡 練 生

發文日期：西元 2003 年 4 月 日
Issue Date

發文字號：09220362280
Serial No.

ATTORNEY/DOCKET No:

DECLARATION FOR PATENT APPLICATION AND APPOINTMENT OF ATTORNEY

As a below named inventor, I hereby declare that my residence, post office address and citizenship are as stated below next to my name; I believe that I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention (Design, if applicable) entitled:

INTERRUPT-PROCESSING SYSTEM FOR SHORTENING INTERRUPT LATENCY IN MICROPROCESSOR

the specification of which (check one):

☒ is attached hereto;

or ☐ was filed on:

as U.S. Application Number or PCT International Application Number;

and (if applicable) was amended on:

I hereby state that I have reviewed and understand the contents of the above-identified specification, including the claims, as amended by any amendment(s) referred to above. I acknowledge the duty to disclose information which is material to patentability as defined in Title 37, Code of Federal Regulations, §1.56. I hereby claim foreign priority benefits under Title 35, United States Code §119 of any foreign application(s) for patent or inventor's certificate listed below and have also identified below any foreign application for patent or inventor's certificate having a filing date before that of the application on which priority is claimed.

PRIOR FOREIGN APPLICATION(S)

PRIORITY CLAIMED

Number	Country	Day/Month/Year Filed	Yes	No
092105040	Taiwan	07/03/2003	<input checked="" type="checkbox"/>	

☐ Additional Priority Application(s) Listed on Following Page(s)

I HEREBY CLAIM THE BENEFIT UNDER TITLE 35 U.S. CODE § 119(E) OF ANY U.S. PROVISIONAL APPLICATIONS LISTED BELOW.

Application Number

Day/Month/Year Filed

☐ Additional Provisional Application(s) Listed on Following Page(s)

I hereby claim the benefit under Title 35, United States Code, §120 of any United States application(s) or PCT international application(s) designating The United States of America listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in that/those prior application(s) in the manner provided by the first paragraph of Title 35, United States Code, §112, I acknowledge the duty to disclose information which is material to patentability as defined in Title 37, Code of Federal Regulations, §1.56 which became available between the filing date of the prior applications) and the national or PCT international filing date of this application:

Application Number

Filing Date

Status - Patented, Pending or Abandoned

☐ Additional US/PCT Priority Application(s) listed on Following Page(s)

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

POWER OF ATTORNEY: I (We) hereby appoint as my (our) attorneys, with full powers of substitution and revocation, to prosecute this application and transact all business in the Patent and Trademark Office connected therewith:

Bruce H. Troxell Reg. No. 26,592

I (we) authorize my (our) attorneys to accept and follow instructions from Novatek Microelectronic Co. regarding any matter related to the preparation, examination, grant and maintenance of this application, any continuation, continuation-in-part or divisional based thereon, and any patent resulting therefrom, until I (we) or my (our) assigns withdraw this authorization in writing.

SEND CORRESPONDENCE TO:

TROXELL LAW OFFICE PLLC
5205 LEESBURG PIKE, SUITE 1404
FALLS CHURCH, VA. 22041

TELEPHONE CALLS TO:

BRUCE H. TROXELL
(703) 575-2711

FULL NAME OF FIRST OR SOLE INVENTOR: Pachinco Yang

CITIZENSHIP: Taiwan

RESIDENCE ADDRESS: c/o Novatek Microelectronic Co., 2F, No. 13, Innovation Road I, Science-Based Industrial Park, HsinChu 300, Taiwan, R.O.C.

POST OFFICE ADDRESS IS THE SAME AS RESIDENCE ADDRESS UNLESS OTHERWISE SHOWN BELOW

DATE

19/08/2003

SIGNATURE

Pachinco Yang

☐ See following page(s) for additional joint inventors.